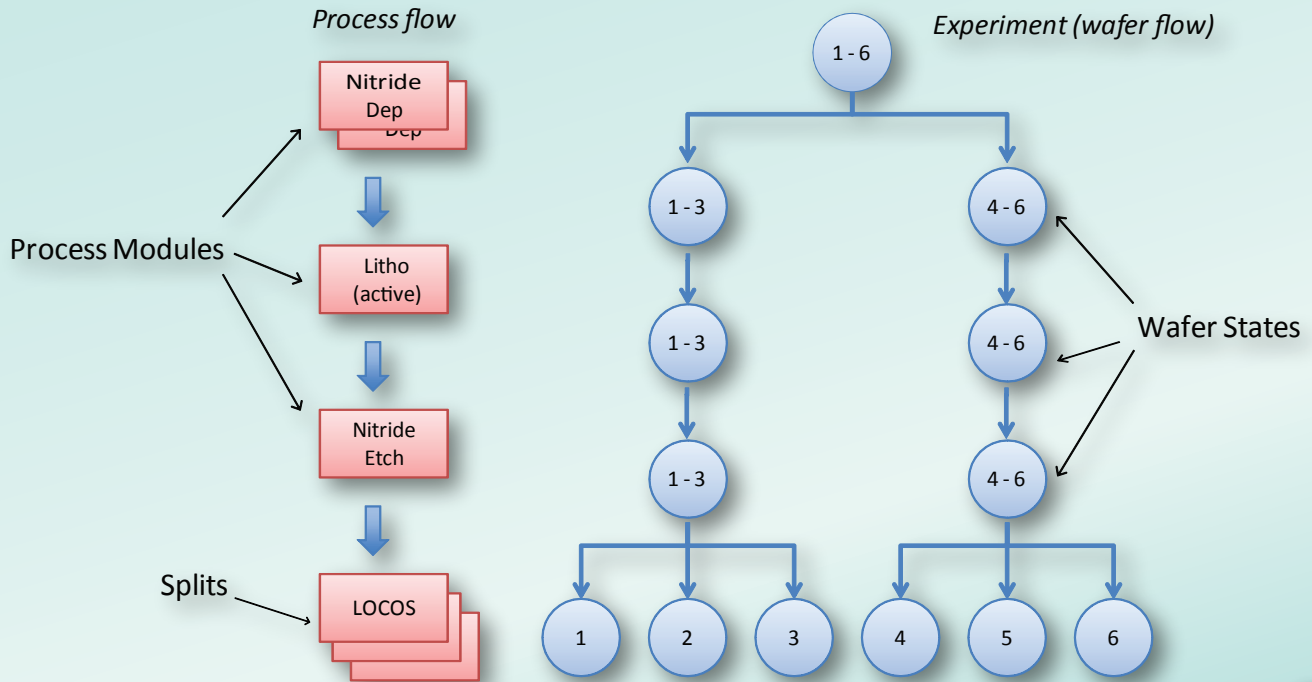


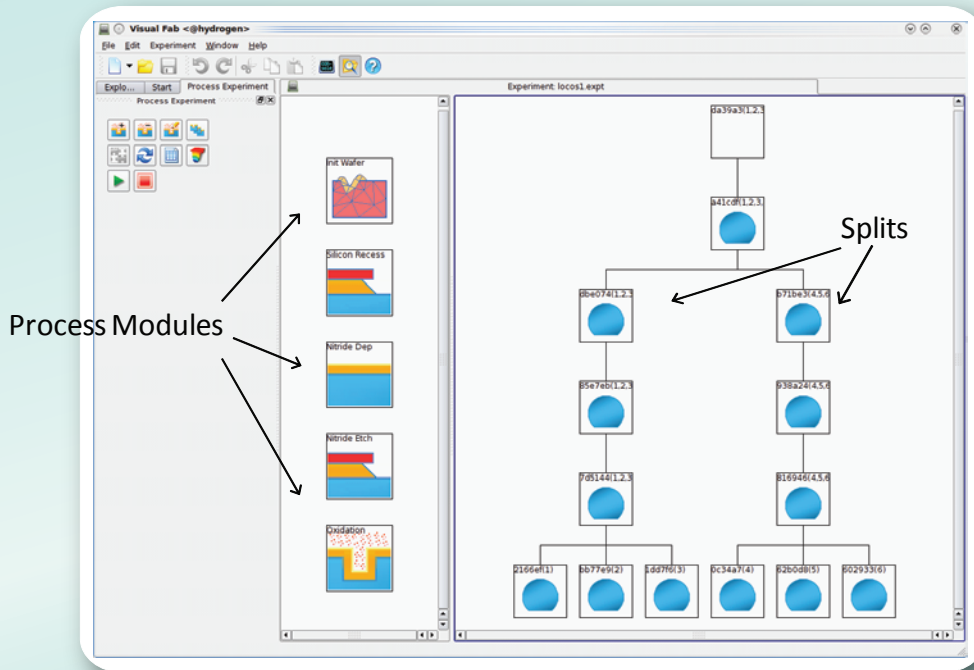
VisualFab The Wafer Fab on Your Desktop

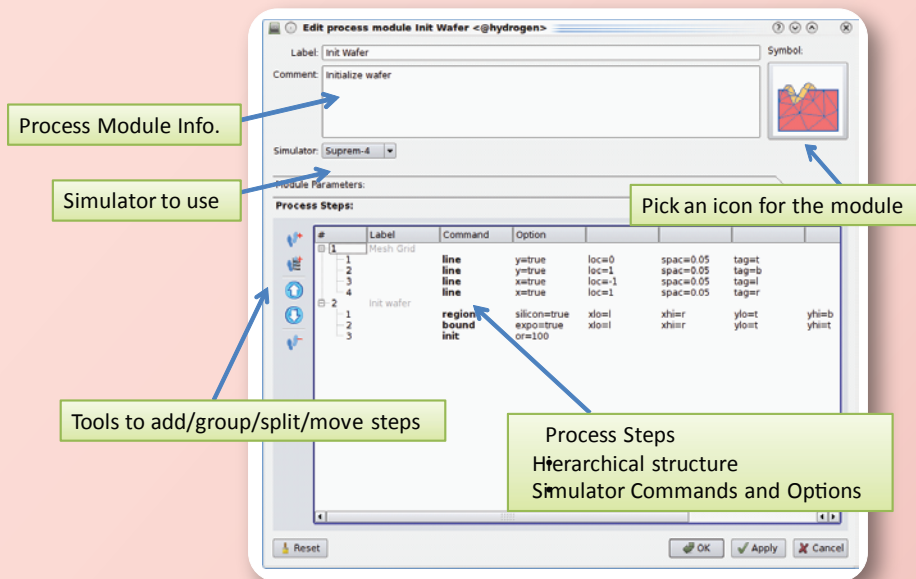
VisualFab is an integrated workbench for process simulation experiments. It adopts the concepts familiar to fab engineers, e.g. process modules, splits, wafer and split table, and visualizes them in a user-friendly GUI.

Fab Engineers' Perspective



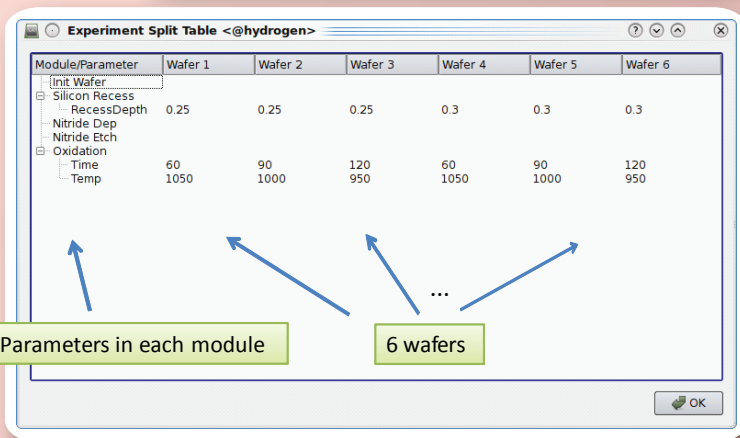
VisualFab Experiment View





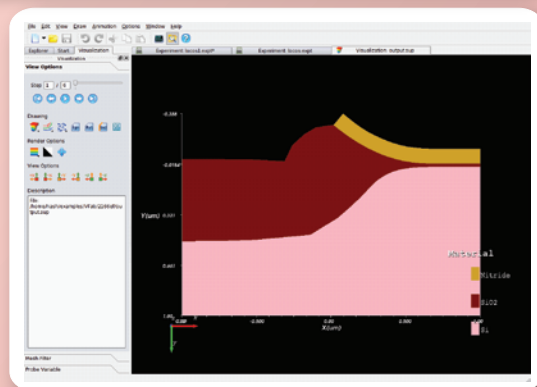
One can edit the process steps in the process module. Process steps can be grouped, and infinite group hierarchy is supported.

Process steps are described with the commands of your favorite process simulator. Multiple popular simulators are supported.



Each process module can have variables, e.g. annealing time and annealing temperature. One can create splits of a module, using different values of the variables.

The split table summarizes all design variables and splits in the experiment.



VisualFab drives the process simulators to simulate each split of each process module, and produce the corresponding wafer states.

One can examine the simulated wafer states, a LOCOS isolation in this case, in the built-in visualizer.

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